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	Subclass
Class	
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

<i>JCT/hasp</i>		O.I.P.E. <i>Sub CTH 34243</i> SCANNED	PATENT DATE <i>Q.A. Am</i>
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/938106		257	185	2811	

APPLICANTS
James Derderian

TITLE

Assemblies including stacked semiconductor devices separated by discrete conductive elements therebetween, packages including the assemblies; and methods

PTO-2040
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL <input type="checkbox"/> DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____ _____				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	

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